



AMD-E306

PATENT

#10/B<sub>(n<sub>i</sub>)</sub>

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of: )  
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                        )  
                        SOBEK, et al.      )      Examiner: Vu, Quang D  
                        )      )  
Serial Number:      09/885,426      )      Art Unit: 2811  
                        )      )  
Filing Date:        June 19, 2001      )      )  
                        )  
For:     A SILICIDED BURIED BITLINE      )  
                        )  
                        PROCESS FOR A NON-      )  
                        )  
                        VOLATILE MEMORY CELL      )  
                        )

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

## AMENDMENT AND RESPONSE

Dear Sir:

In response to the Office Action mailed on May 9, 2003, Applicants respectfully request the Examiner to enter the following amendments and further examination and reconsideration of the above captioned patent application in view of the remarks set forth below.

RECEIVED  
JUL 17 2003  
TECHNOLOGY CENTER 28000

AMD-E306/JPH/MJB  
Examiner: Vu, Quang D.

- 1 -

Serial No.: 09/885,426  
Group Art Unit: 2811